

Representative data parallelism: Loop Parallelism

- Many algorithms perform computations by iteratively traversing a large data structure
 - Commonly expressed as a loop
- *If the iterations are independent:*
 - Iterations can be executed in arbitrary order and in parallel on different processors

Remark

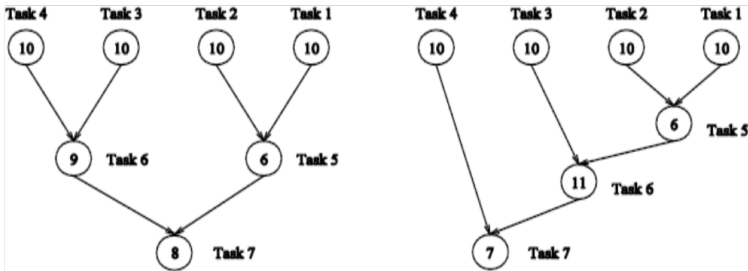
OpenMP is a widely used “shortcut” to achieve loop parallelism in C/C++. We will cover OpenMP later.

Task Dependence graph

- Can be used to visualize and evaluate the task decomposition strategy
- A **directed acyclic graph**:
 - Node: Represent each task, node value is the expected execution time
 - Edge: Represent **control dependency** between task
- Properties:
 - Critical Path Length: Maximum (slowest) completion time
 - Degree of concurrency = Total Work / Critical Path Length
 - An indication of amount of work that can be done concurrently

Task Dependence Graph - Example

- Two different TDGs for the same program:

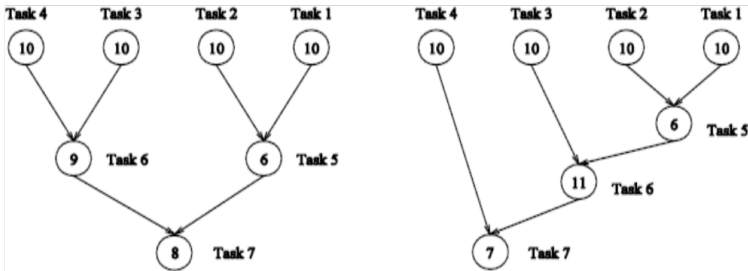


Critical Path Length: Maximum (slowest) completion time
 Degree of concurrency = Total Work / Critical Path Length

- An indication of amount of work that can be done concurrently

Task Dependence Graph - Example

- Two different TDGs for the same program:



Critical Path = (Task 4 → 6 → 7)
 Critical Path Length = 27
 Degree of concurrency = 63 / 27 = 2.33

Critical Path = (Task 1 → 5 → 6 → 7)
 Critical Path Length = 34
 Degree of concurrency = 64 / 34 = 1.88

2. Communication (Coordination)

- Tasks are intended to execute in parallel
 - but generally not executing independently
 - need to determine data passed among tasks
- Ideally, distribute and overlap computation and communication

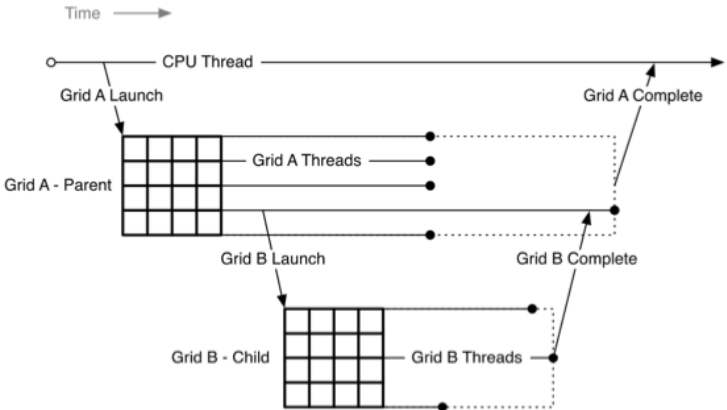
Coordination/Communication Models

- No communication
- Shared address space
- Message passing

Communication Models: No Communication

- Historically: same operation on each element of an array
 - SIMD, vector processors
- Basic structure: map a function onto a large collection of data
 - Functional: side-effect free execution
 - No communication among distinct function invocations
 - Allows invocations to be scheduled in parallel
 - Stream programming model
- Modern performance-oriented data-parallel languages do not strictly enforce this structure
 - CUDA, OpenCL

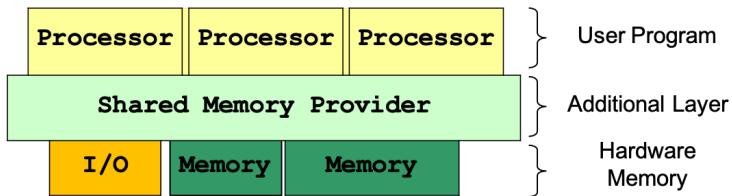
CUDA Execution



Communication Models: Shared Address Space

- Communication abstraction
 - Tasks communicate by reading/writing to shared variables
 - Ensure mutual exclusion via use of locks
 - Logical extension of uniprocessor programming
- Requires hardware support to implement efficiently
 - Any processor can load and store from any address
 - Even with NUMA, costly to scale
 - Matches shared memory systems – UMA, NUMA, etc.

Shared-Memory Communication



Examples

Typical forms of shared-memory communication include "broadcast", "reduction", etc.

Communication Models: Message Passing

- Tasks operate within their own private address spaces
 - Tasks communicate by *explicitly sending/receiving messages*
- Popular software library: MPI (Mostly for C++), RMI & Sockets (Mostly for Java)
- Hardware does not implement system-wide loads and stores
 - Can connect commodity systems together to form large parallel machine
- Matches distributed memory systems
 - Programming model for clusters, supercomputers, etc

Correspondence with Hardware Implementations

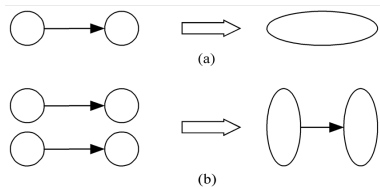
- Common to implement message passing abstractions on machines that implement a shared address space in hardware
 - “Sending message” = copying memory from message library buffers
 - “Receiving message” = copy data from message library buffers
- Possible to implement shared address space abstraction on machines that do not support it in HW
 - Less efficient software solutions
 - Mark all pages with shared variables as invalid
 - Page-fault handler issues appropriate network requests

Summary of Coordination Models

- No communication:
 - Programs perform same function on different data elements in a collection
- Shared address space:
 - All threads can read and write to all shared variables
 - Drawback: not all reads and writes have the same cost (and that cost is not apparent in program text), and may lead to implicit conflict (dangerous!)
- Message passing:
 - All communication occurs in the form of explicit messages

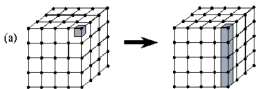
Motivation of Agglomeration

- Eliminate communication between primitive tasks agglomerated into consolidated task
- For example, combine groups of sending and receiving tasks

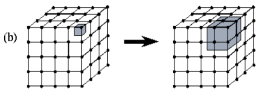


Reduce number of
sends and receives

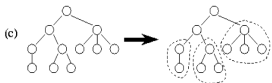
Examples of Agglomeration



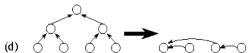
- Reduce dimension of decomposition from 3 to 2



- 3-D decomposition (adjacent tasks are combined)



- Divide-and-conquer – sub-tree are coalesced



- Tree algorithm – nodes are combined

Agglomeration Rules of Thumb

- Locality of parallel algorithm has increased
- Number of tasks increases with problem size
- Number of tasks suitable for likely target systems
- Tradeoff between agglomeration and code modifications costs is reasonable

